

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|-----------------|------------------|---------|------------------|
| L1 | 27 | (device or computer or controller) adj address same (local adj address) | USPAT | OR | OFF | 2004/12/02 10:52 |
| L2 | 18 | (device or computer or controller) adj address same (local adj address)same memory | USPAT | OR | OFF | 2004/12/02 10:44 |
| L3 | 3 | geographic adj address same local adj address | US-PGPUB; USPAT | OR | OFF | 2004/12/02 10:47 |
| L4 | 2 | meta adj address and baxter.in. | US-PGPUB; USPAT | OR | OFF | 2004/12/02 10:48 |
| L5 | 16 | baxter.in. and Ricoh.as. | US-PGPUB; USPAT | OR | OFF | 2004/12/02 10:48 |
| L6 | 14 | (device or computer or controller) adj address same (local adj address) and @ad<"19980226" | USPAT | OR | OFF | 2004/12/02 10:52 |
| S1 | 94 | ((DYNAMICALLY ADJ RECONFIGURABLE) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3)) | USPAT | OR | OFF | 2004/12/01 18:32 |
| S2 | 1216 | ((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3)) | USPAT | OR | OFF | 2004/12/01 18:33 |
| S3 | 543 | ((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR)) | USPAT | OR | OFF | 2004/12/01 18:33 |
| S4 | 14 | ((DPGA OR FPGA) SAME (INSTRUCTION ADJ SET)) | USPAT | OR | OFF | 2004/12/01 18:33 |
| S5 | 9 | (((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR)) AND (INSTRUCTION ADJ SETS)) | USPAT | OR | OFF | 2004/12/01 18:33 |
| S6 | 1 | ("5794062".PN.) AND (INSTRUCTION ADJ SET)) | USPAT | OR | OFF | 2000/03/21 10:52 |
| S7 | 1 | ("5684980".PN.) AND ARCHITECTURE) | USPAT | OR | OFF | 2000/03/21 10:52 |
| S8 | 186 | ((DYNAMICALLY ADJ RECONFIGURABLE) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3)) | USPAT | OR | OFF | 2004/12/01 18:33 |
| S9 | 4396 | ((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR OR PROCESS\$3)) | USPAT | OR | OFF | 2004/12/01 18:33 |
| S10 | 2563 | ((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR)) | USPAT | OR | OFF | 2004/12/01 18:33 |

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|-----|--------|--|-------|----|-----|------------------|
| S11 | 81 | ((DPGA OR FPGA) SAME (INSTRUCTION ADJ SET)) | USPAT | OR | OFF | 2004/12/01 18:33 |
| S12 | 45 | (((DPGA OR PGA OR FPGA) SAME (MICROPROCESSOR OR PROCESSOR)) AND (INSTRUCTION ADJ SETS)) | USPAT | OR | OFF | 2004/12/01 18:33 |
| S13 | 262471 | geographic adj address same local address | USPAT | OR | OFF | 2004/12/01 18:34 |
| S14 | 2 | geographic adj address same local adj address | USPAT | OR | OFF | 2004/12/02 10:46 |